

U.S. PATENT DOCUMENTS

~~GROUP 2100~~

## USPTO Form 1449 [R&amp;P]

FORM PTO-1449 (Rev. 2-32)	U.S. DEPARTMENT OF COMMERCE PATENT AND TRADEMARK OFFICE	ATTY. DOCKET NO. YAO-3950	SERIAL NO 09/103,873
Information Disclosure Statement by Applicant		Applicant Yoshihisa Nagano et al.	
(Use several sheets if necessary)		FILING DATE June 24, 1998	GROUP 2815

## U.S. PATENT DOCUMENTS

Exmr Initial	Document Number	Date	Name	Class	Sub Class	Filing Date
<i>[initials]</i>	5,344,797	09/06/1994	Pai et al.			
<i>[initials]</i>	5,374,578	12/20/1994	Patel et al.			
<i>[initials]</i>	5,429,840	08/08/1995	Jones, Jr., et al.			
<i>[initials]</i>	5,503,882	04/02/1996	Dawson			
<i>[initials]</i>	5,523,595	06/04/1996	Takenaka et al.			
<i>[initials]</i>	5,650,359	07/22/1997	Ahlburn			
<i>[initials]</i>	5,747,830	05/05/1998	Okita			
<i>[initials]</i>	5,815,223	09/29/1998	Watanabe et al.			
<i>[initials]</i>	5,872,401	02/16/1999	Huff et al.			

## FOREIGN PATENT DOCUMENTS

Exmr Initial	Document Number	Date	Country	Class	Sub Class	Translation YES   NO
<i>[initials]</i>	6-291317	10/18/1994	Japan (with English abstract)			

## OTHER DOCUMENTS

(Including Author, Title, Date, Pertinent Pages, Etc.)

<i>[initials]</i>	1)	S. Wolf Ph.D., et al., "Silicon Processing for the VLSI Era", PROCESS TECHNOLOGY, Volume 1, pp. 189-191, 7/22/99

Examiner <i>[signature]</i>	Date Considered 6/29/00
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Examiner: Initial if citation considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.